

Fig. 1



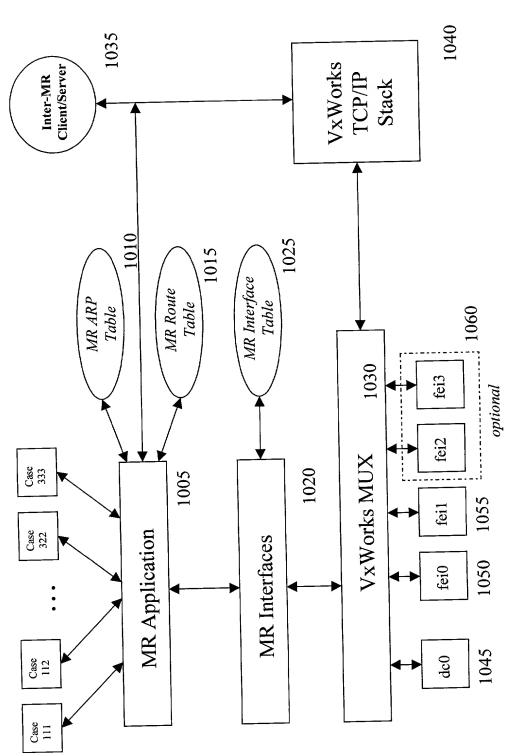


Fig. 2

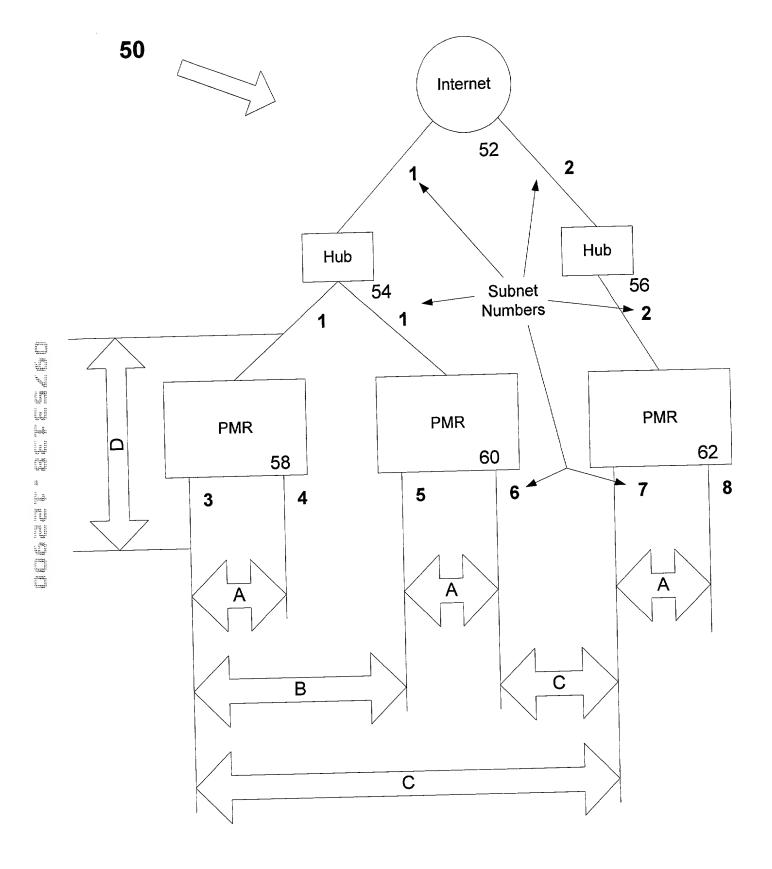


FIG. 3

**Processing Flow** 

120

130

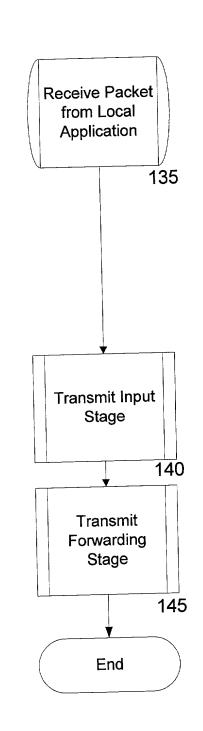
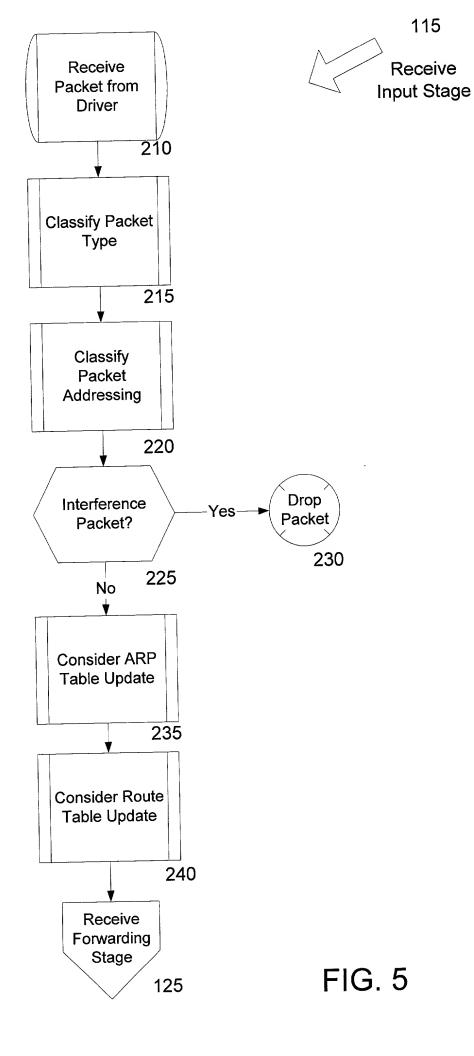


FIG. 4A

FIG. 4B



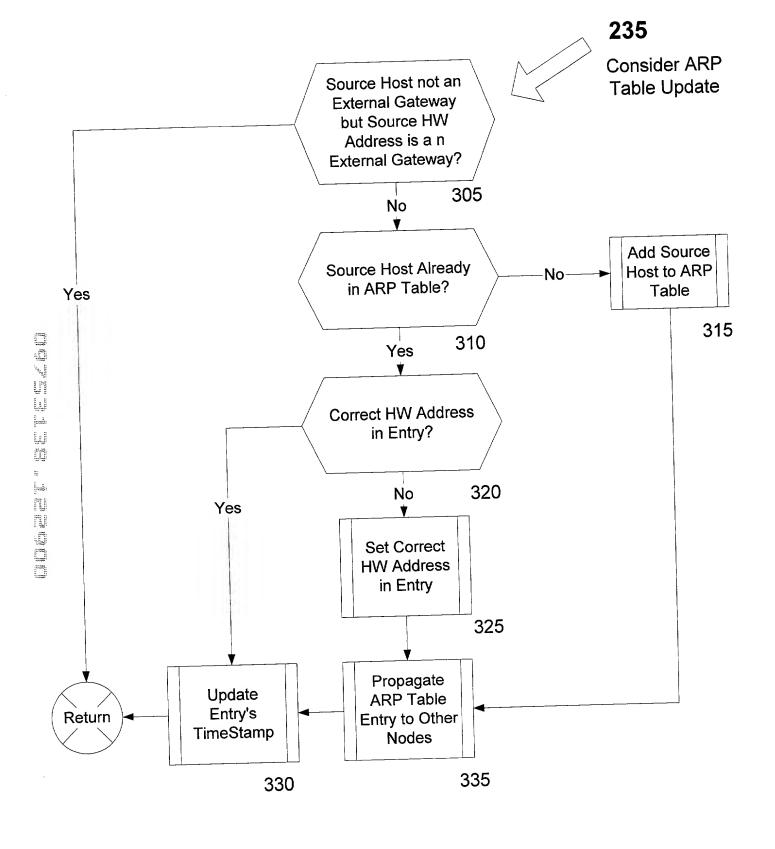


FIG. 6

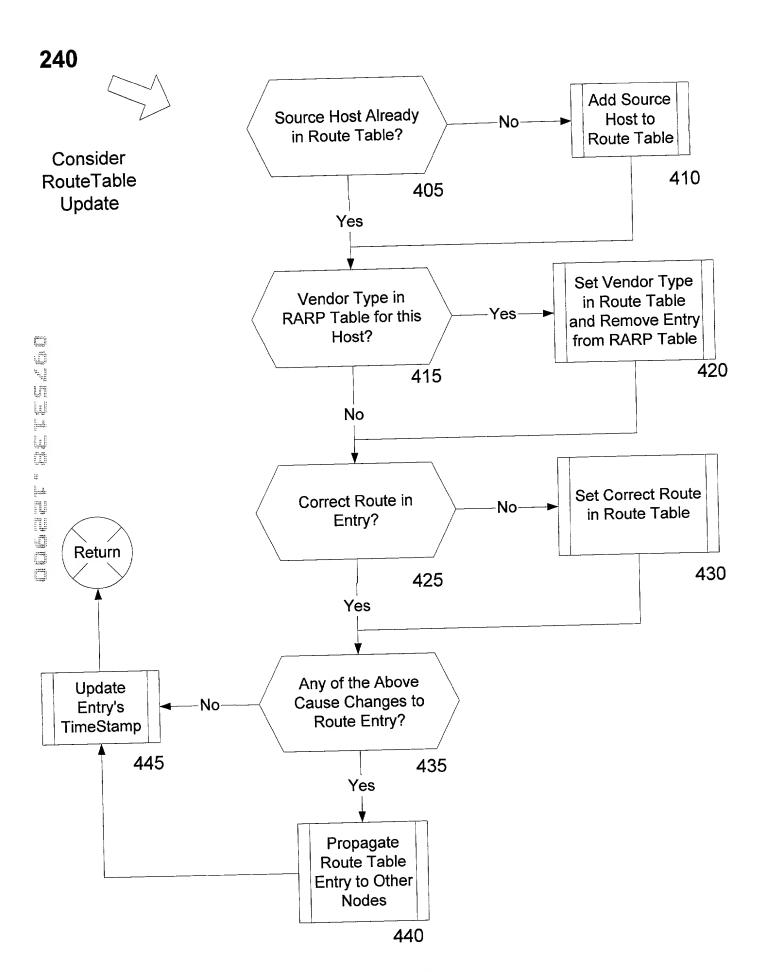


FIG. 7

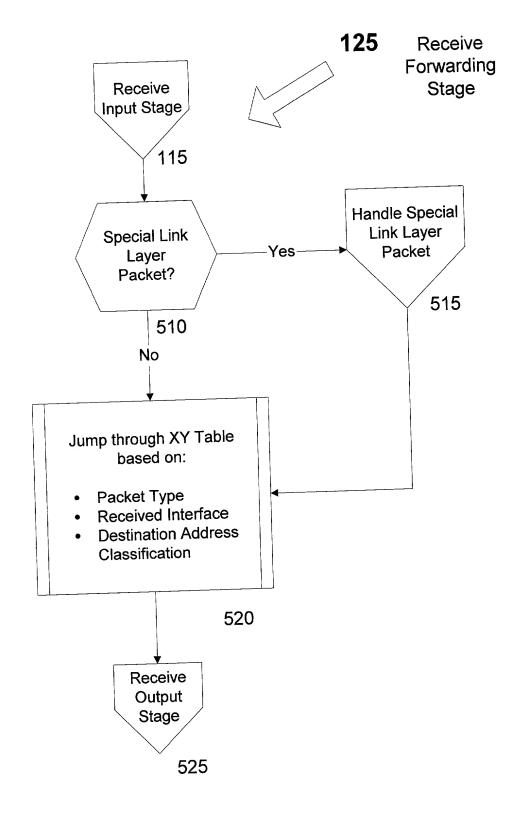


FIG. 8



Special Link Layer Packet Stage

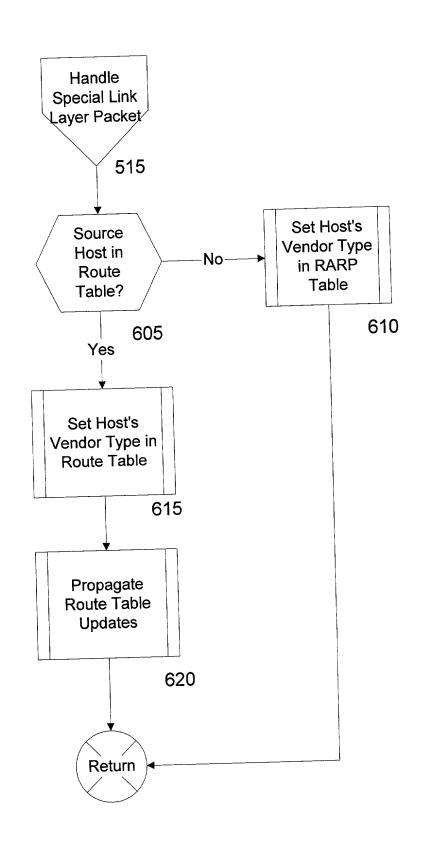
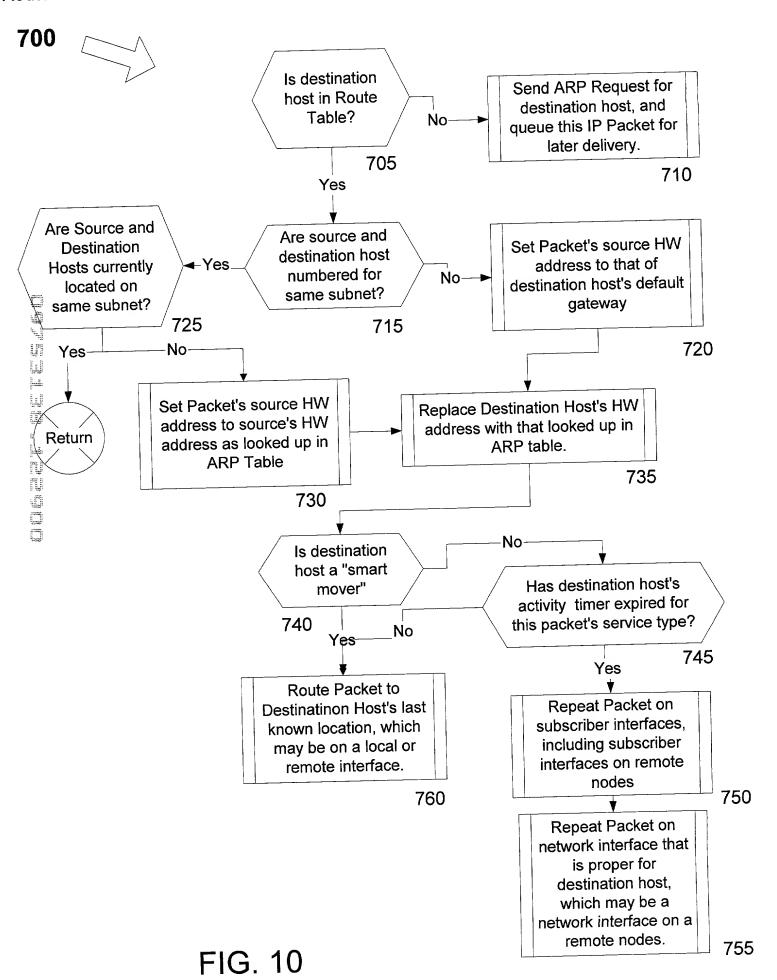


FIG. 9



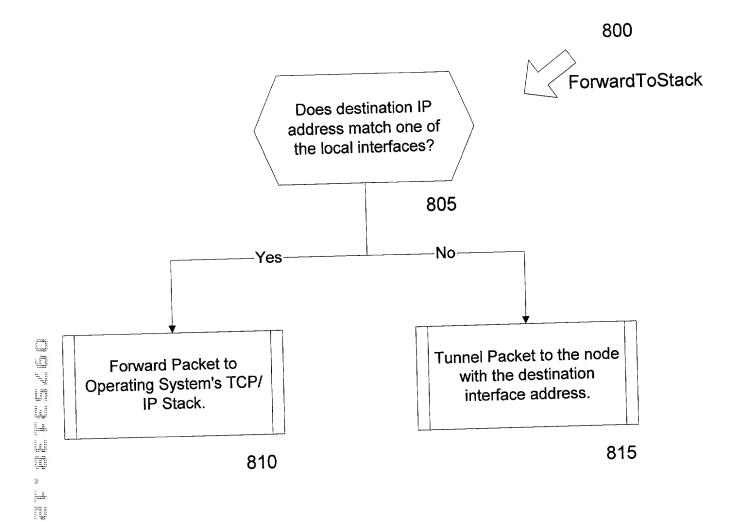


FIG. 11

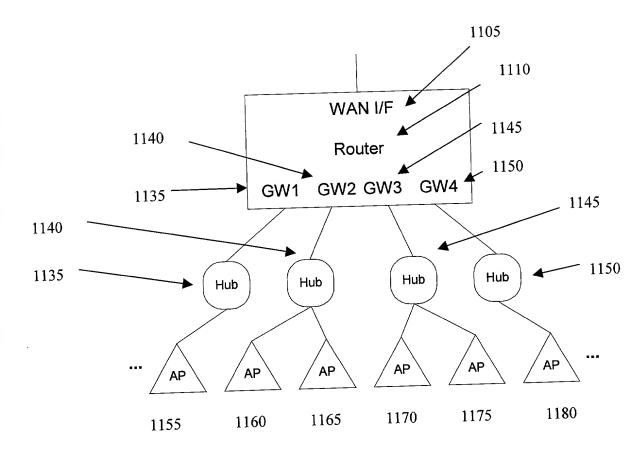


FIG. 12 (PRIOR ART)

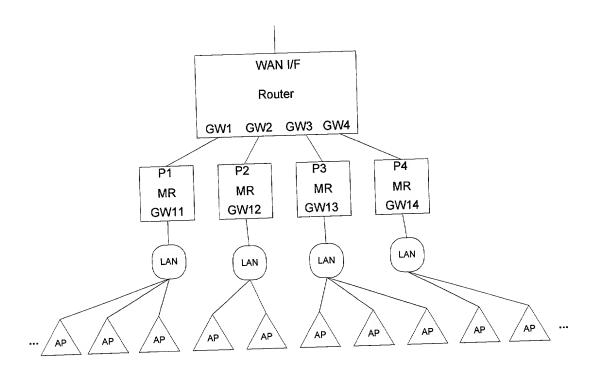


FIG. 13

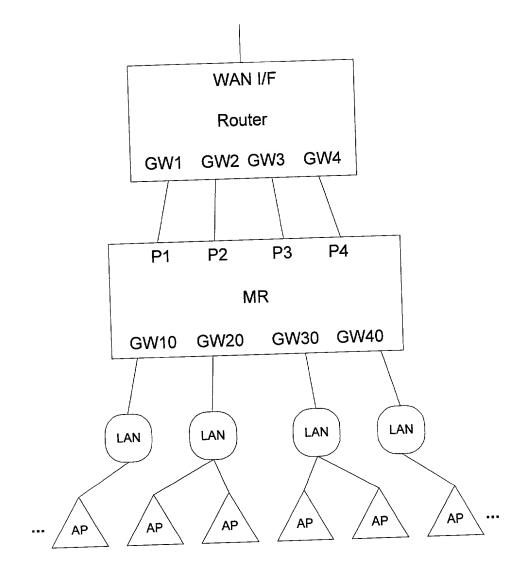


FIG. 14

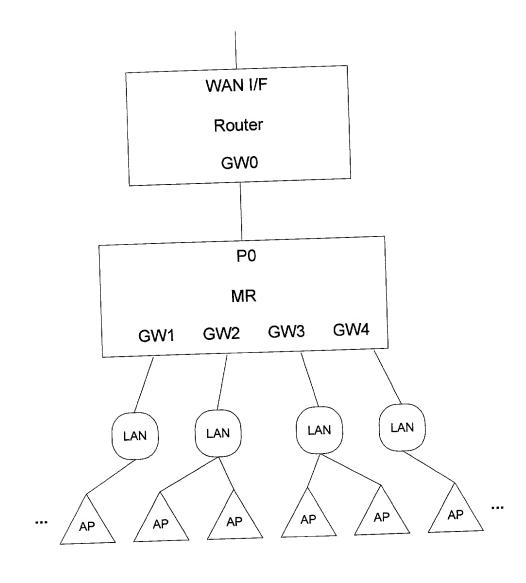


FIG. 15

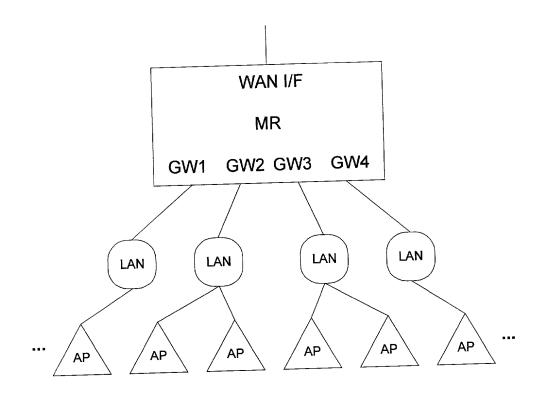


FIG. 16

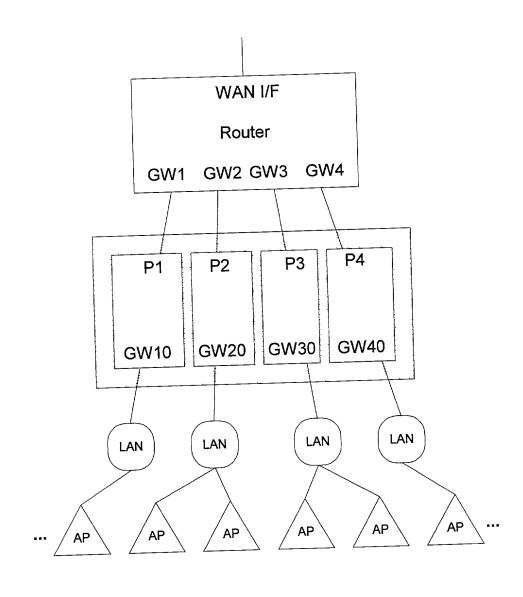


FIG. 17

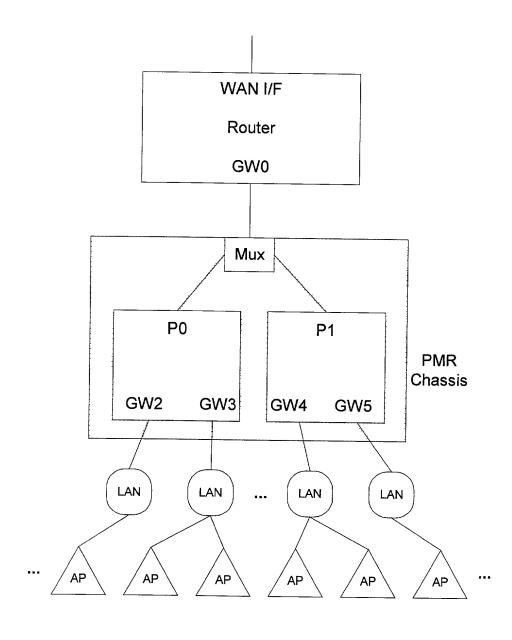


FIG. 18

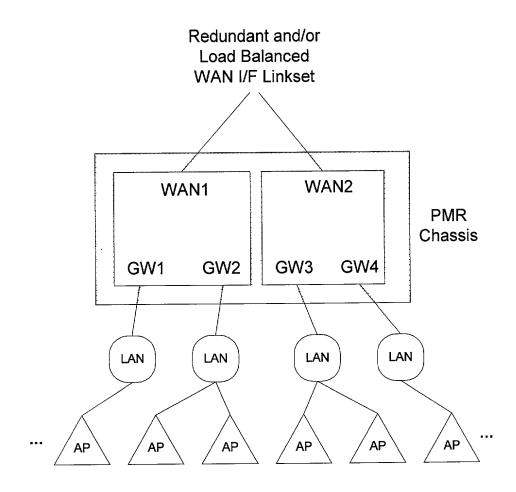


FIG. 19

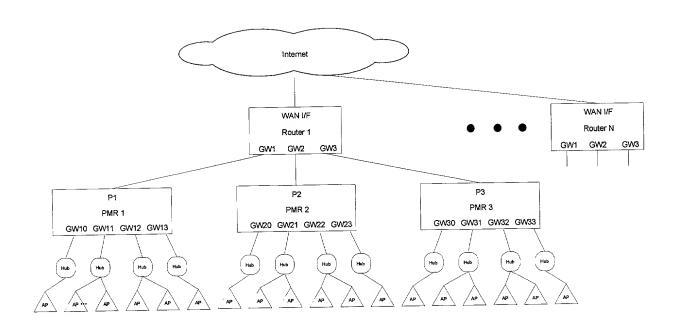


FIG. 20

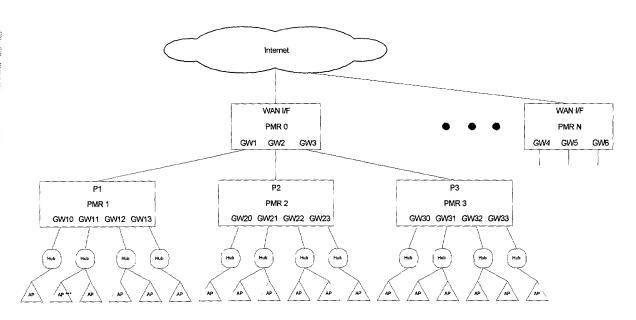


FIG. 21

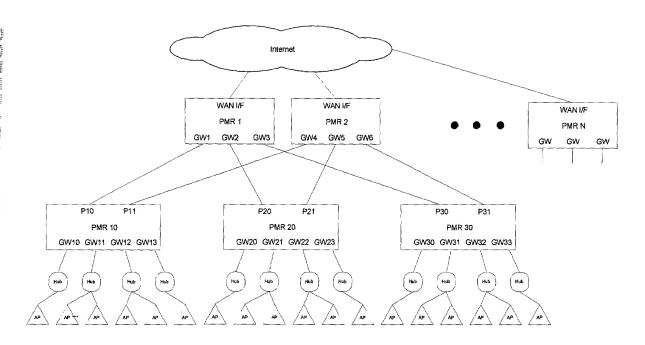


FIG. 22